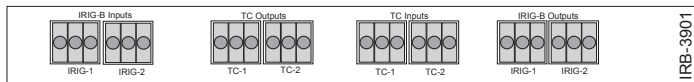


## DESCRIPTION

The IRB-3901 is a highly versatile IRIG-B to SMPTE/EBU timecode conversion module. The IRB-3901 allows the user to interface IRIG-B time systems to the CSD-3901 giving the user the ability to easily interface the CSD-3901 into existing systems, simplifying installation and minimizing installation costs.

This module, as is the case with all modules in the NEO family, can be controlled locally via a front-edge display or by remote communications allowing for remote control, monitoring and diagnostics using hardware control panels and/or a GUI.



## INPUTS

### BALANCED NON-DROP LTC

Standard	SMPTE 12/EBU
Connector	WMR
Impedance	Hi-Z, balanced
Signal level	4 Vp-p, ±8 dB

### UNBALANCED IRIG-B

Standards	IRIG 200-98
Format	B122
Connector	BNC per IEC 169-8
Impedance	Hi-Z
Level	5 V p-p

## FEATURES

- ▶ **Simultaneous conversion of IRIG-B to SMPTE/EBU timecode and SMPTE/EBU timecode to IRIG-B**
- ▶ **2 IRIG-B inputs**
- ▶ **2 IRG-B outputs, each with selectable timecode inputs**
- ▶ **2 SMPTE/EBU timecode inputs**
- ▶ **2 SMPTE/EBU timecode outputs, each with selectable IRIG-B inputs**
- ▶ **Output locked to input timecode**
- ▶ **Command Control System (CCS) Enabled**

## OUTPUTS

### BALANCED NON-DROP LTC

Standards	SMPTE 12/EBU
Connector	WMR
Impedance	Low-Z, balanced
Signal level	4 Vp-p, ±8 dB

### UNBALANCED IRIG-B

Standards	IRIG 200-98
Format	B122
Connector	BNC per IEC 169-8
Impedance	Low-Z
Level	5 V p-p, ±10%

### MISCELLANEOUS

Free-run timecode precision	Error rate of 8.64 seconds every 24 hours
Power consumption	+24 v, <10 W
Propagation delay	<1μs

## SPECIFICATIONS

Specifications and designs are subject to change without notice.

## ORDERING INFORMATION

For more information visit [www.leitch.com](http://www.leitch.com)

IRB-3901 IRIG-B Interface Module

## RELATED PRODUCTS

CSD-3901 Master Clock Driver

**Note:** See NEO Series Frames and Local Control Panel Section for ordering information on frames, local panels and power supply